



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Akihiko Kimoto

Serial No.: 10/609,491

Filed: 06/25/2003

PATH ERROR MONITORING METHOD AND APPARATUS THEREOF

ART UNIT: 2133

SUBMISSION OF CORRECTED DRAWINGS

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

SIR:

Enclosed please find replacements sheets for Figures
1-3, 12 in the above referenced application.

Any fee due with this paper, not fully covered by an
enclosed check, may be charged to Deposit Acct. No. 50-1290.

Respectfully submitted,

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Date: June 10, 2005
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by